



PRODUCT SPECIFICATION

MODEL: H101BWX40I3502

<◇>PRELIMINARY SPECIFICATION

<◆>APPROVAL SPECIFICATION

CUSTOMER
APPROVED BY
DATE:

DESIGNED	CHECKED	APPROVED

PREPARED BY:



1 Document revision history :

DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY	APPROVED BY

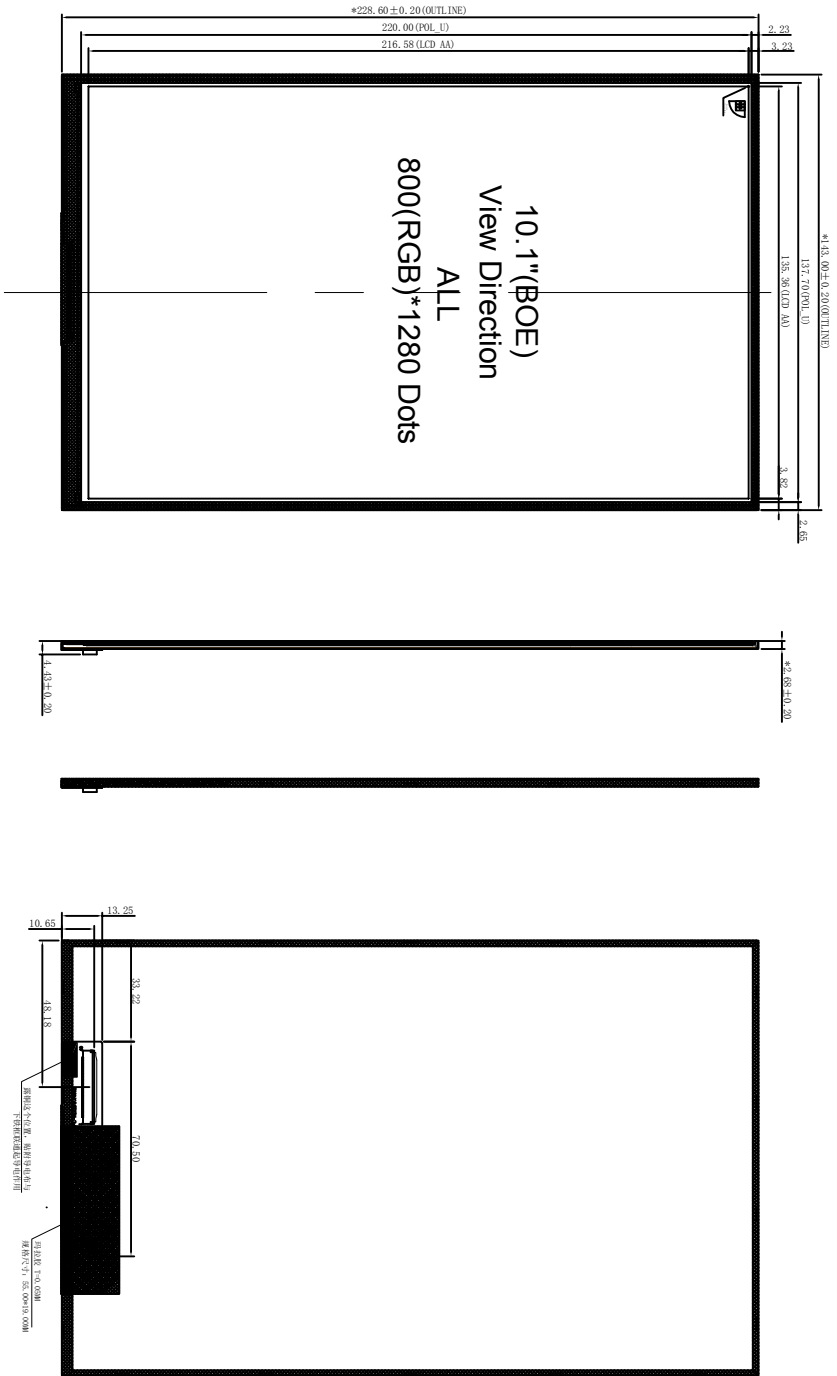


1.2 FEATURES:

No.	Item	Specification	Unit
1	Panel Size	10.1"	inch
2	Number of Pixels	800×RGB (3) ×1280	pixels
3	Active Area	135.36(V)X216.576(H)	mm
4	Pixel Pitch	0.1692(H)X0.1692(V)	mm
5	Outline Dimension	228.6(W)X143(H)X2.68(D)	mm
6	Number of Colors	-	-
7	Display Mode	Normally Black IPS	-
8	Viewing Direction	ALL Viewing Direction	-
9	Display Format	RGB vertical stripe	-
10	Luminance (cd/m ²)	300 (TYP.)	nit
11	Contrast Ratio	800(TYP.)	
12	Surface Treatment	Anti-Glare	-
13	Interface	MIPI	-
14	Backlight	White LED	-
15	NTSC	60(type)	%
16	Operation Temperature	-20~70	°C
17	Storage Temperature	-30~80	°C

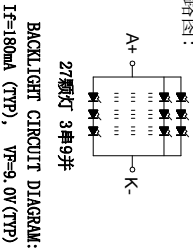
2. Dimensional Outline

REV.	DATE	MODIFICATION
1.0	2021.01.19	Final Issue



Pad No.	Pad Name
1	NC
2	VDD1N
3	VDD1N
4	GND
5	RESET
6	NC
7	GND
8	MIP1-0
9	MIP1-0
10	GND
11	MIP1-1
12	MIP1-1
13	GND
14	MIP1-CI
15	MIP1-CI
16	GND
17	MIP1-2I
18	MIP1-2I
19	GND
20	MIP1-3I
21	MIP1-3I
22	GND
23	NC
24	NC
25	GND
26	NC
27	PMMO
28	NC
29	NC
30	GND
31	LED-
32	LED-
33	NC
34	NC
35	NC
36	NC
37	NC
38	NC
39	LED+
40	LED+

BL 电路图:



- NOTES:
1. DISPLAY TYPE: 10.1INCH TFT /Baick
 2. BACKLIGHT: 27 CHIP WHITE LED, IN PARALLEL
 3. OPERATING TEMP: -20°C~+70°C
 4. STORAGE TEMP: -30°C~+80°C
 5. RESOLUTION: 800(RGB)x1280
 6. LCD IC: TBD
 7. "()"reference dimension. " * "critical dimension
 8. RoHS Compliant

INTERFACE	RGB Interface	MODEL NAME	PART NO.
	FPC Connector	TFT Display Module	H101BW4013502
VIEWING DIRECTION	FREE	DWN	REV. 1.0 SHEET OF 1/1
Gray Scale DIRECTION	ALL	CHKD	TOLERANCE UNLESS SPECIFIED ±0.1
		PROJECTION 3RD ANGLE	UNIT mm SCALE 1:1



3. PIN DESCRIPTION

Pin No.	Symbol	Type	Function
1	NC	-	NC
2	VDDIN	P	Power supply 3.3V
3	VDDIN	P	Power supply 3.3V
4	GND	P	Ground
5	RESET		Device reset signal
6	NC	-	No connection
7	GND	P	Ground
8	MIPI-0N	I	MIPI Date negative signal
9	MIPI-0P	I	MIPI Date Positive signal
10	GND	P	Ground
11	MIPI-1N	I	MIPI Date negative signal
12	MIPI-1P	I	MIPI Date Positive signal
13	GND	P	Ground
14	MIPI-CLKN	I	MIPI clock- negative signal
15	MIPI-CLKP	I	MIPI clock+ Positive signal
16	GND	P	Ground
17	MIPI-2N	I	MIPI Date negative signal
18	MIPI-2P	I	MIPI Date Positive signal
19	GND	P	Ground
20	MIPI-3N	I	MIPI Date negative signal
21	MIPI-3P	I	MIPI Date Positive signal
22	GND	P	Ground
23	NC	-	No connection
24	NC	-	No connection
25	GND	P	Ground
26	NC	-	No connection
27	NC	-	No connection
28	NC	-	No connection
29	NC	-	No connection
30	GND	P	Ground
31	LED-	P	Power for LED backlight (Cathode)
32	LED-	P	Power for LED backlight (Cathode)
33	NC	-	No connection
34	NC	-	No connection
35	NC	-	No connection
36	NC	-	No connection
37	NC	-	No connection
38	NC	-	No connection
39	LED+	P	Power for LED backlight (Anode)
40	LED+	P	Power for LED backlight (Anode)



4. Electrical Characteristics

4-1 TFT LCD Module Operating Conditions

Item	Symbol	Min	Type	Max	Unit	NOTE
Supply Voltage	VDD	3.0	3.3	3.6	V	
Power IC provides voltage	AVEE	-6	-5	-4.5	V	
	AVDD	4.5	5.5	6.0	V	
Driver IC provides voltage internally	调整对比度，调大颜色变深，调小颜色变浅					
	VGH	9	18	20	V	
	VGL	-20	-10	-9	V	
	VIH	-20	-	VCC	V	
	VIL	-30	-	0.3VCC	V	

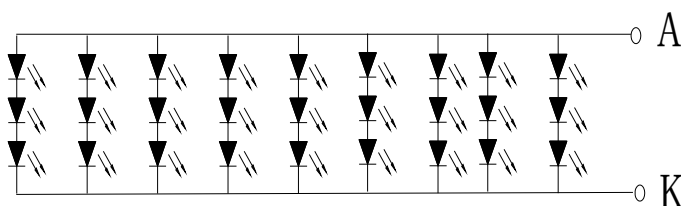
LED back light specification (per a chip)

4.2 BACK LIGHT UNIT

Ta=25℃

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	I _{LED}		180		mA	27LEDS
Forward voltage	V _F	8.4	9.0	9.6	V	I _F =180mA 21LEDS
Reverse current	I _R			50	μA	V _R =3V, 1LED
Power dissipation	P _d	1890			mW	27LEDS
Peak forward current	I _{FP}	20			mA	1LED
Reverse Voltage	V _R	3			V	1LED

4.5.1 Internal Circuit Diagram

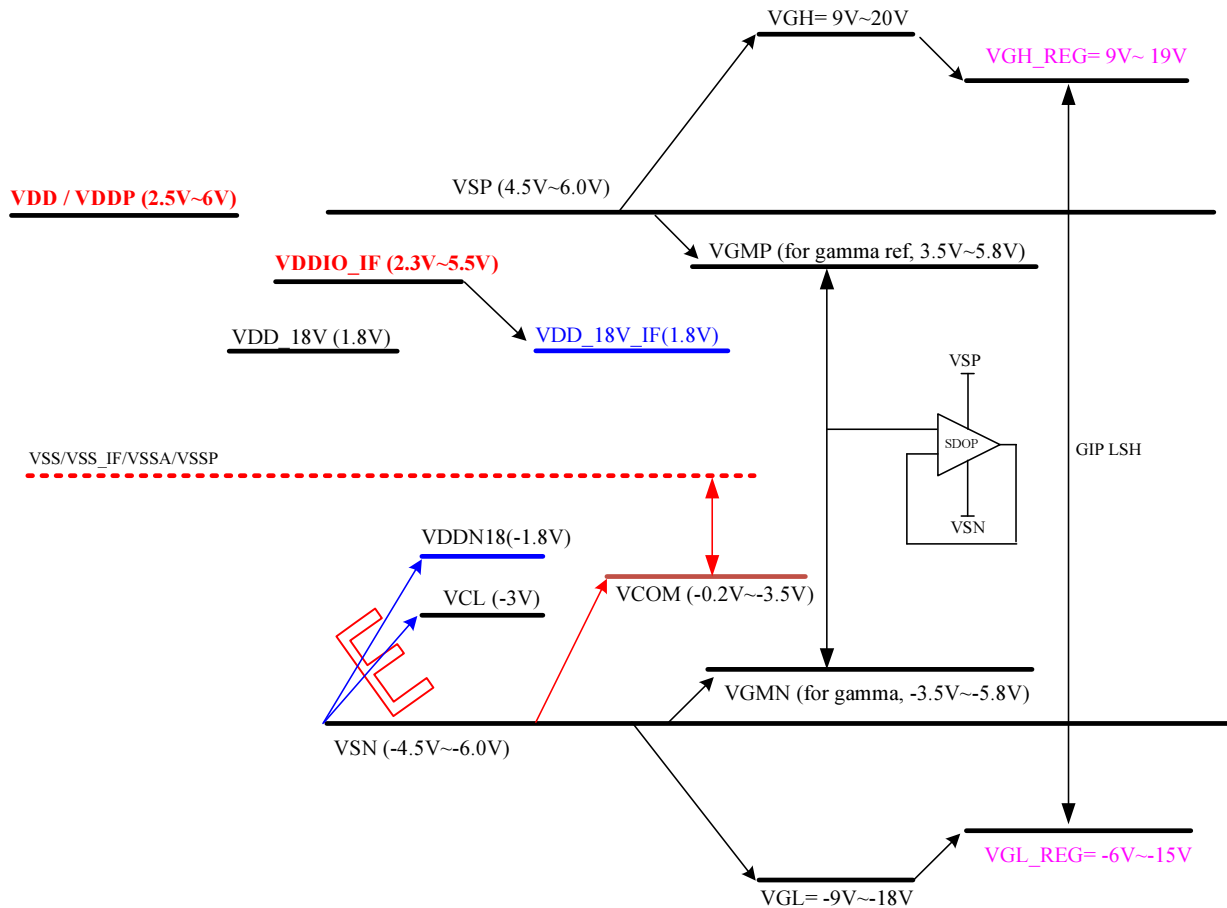


3X9=27LED 9.0V 20X9=180mA



4-2 Power Generation

4.2 Power Generation





5. Power on/off sequence

5.1 Power on sequence PMODE[1:0]=00b

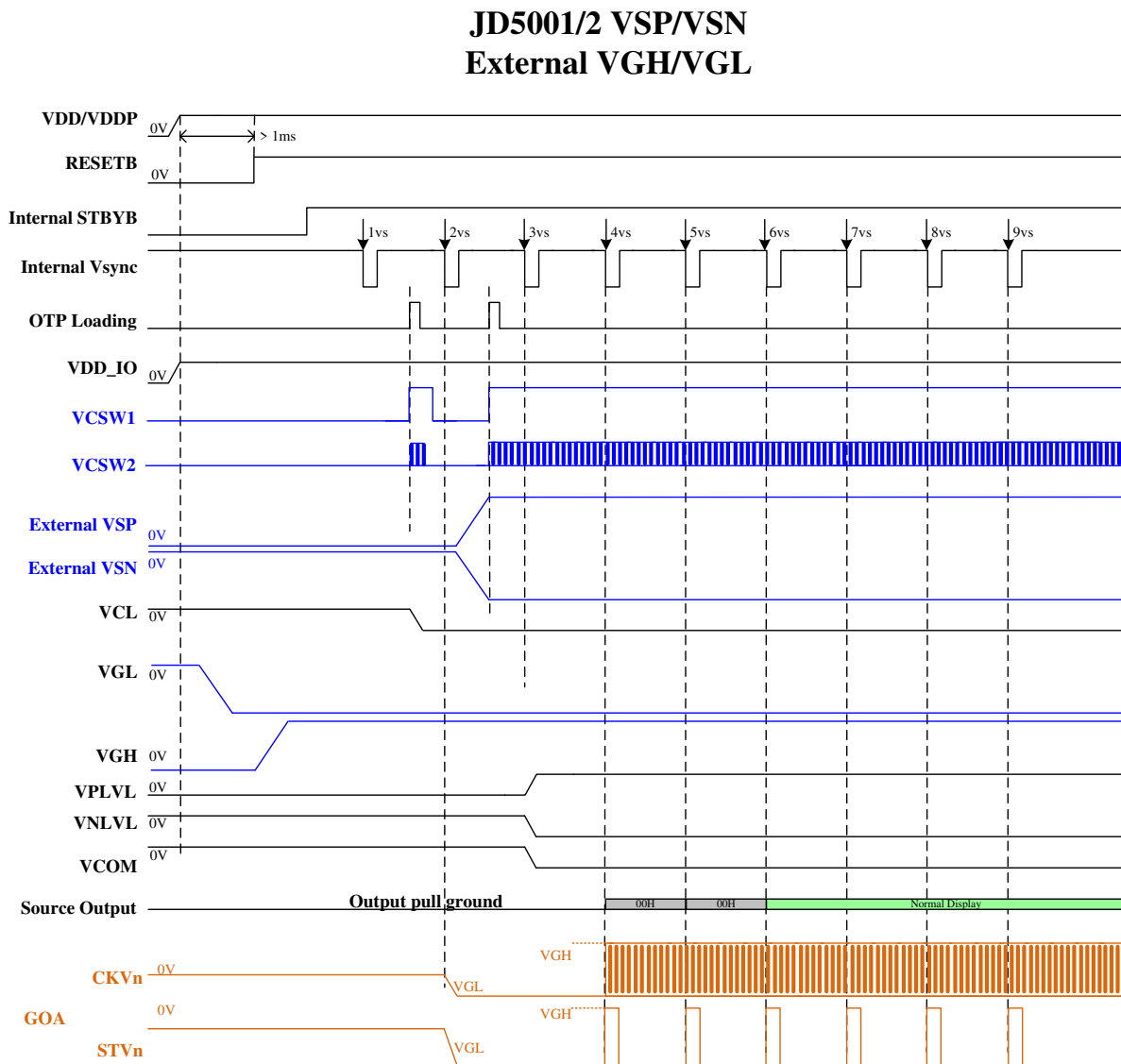


Figure 5.1: Power on sequence with PMODE[1:0]=00b



5.2 Power off sequence PMODE[1:0]=00b

JD5001/2 VSP/VSN External VGH/VGL

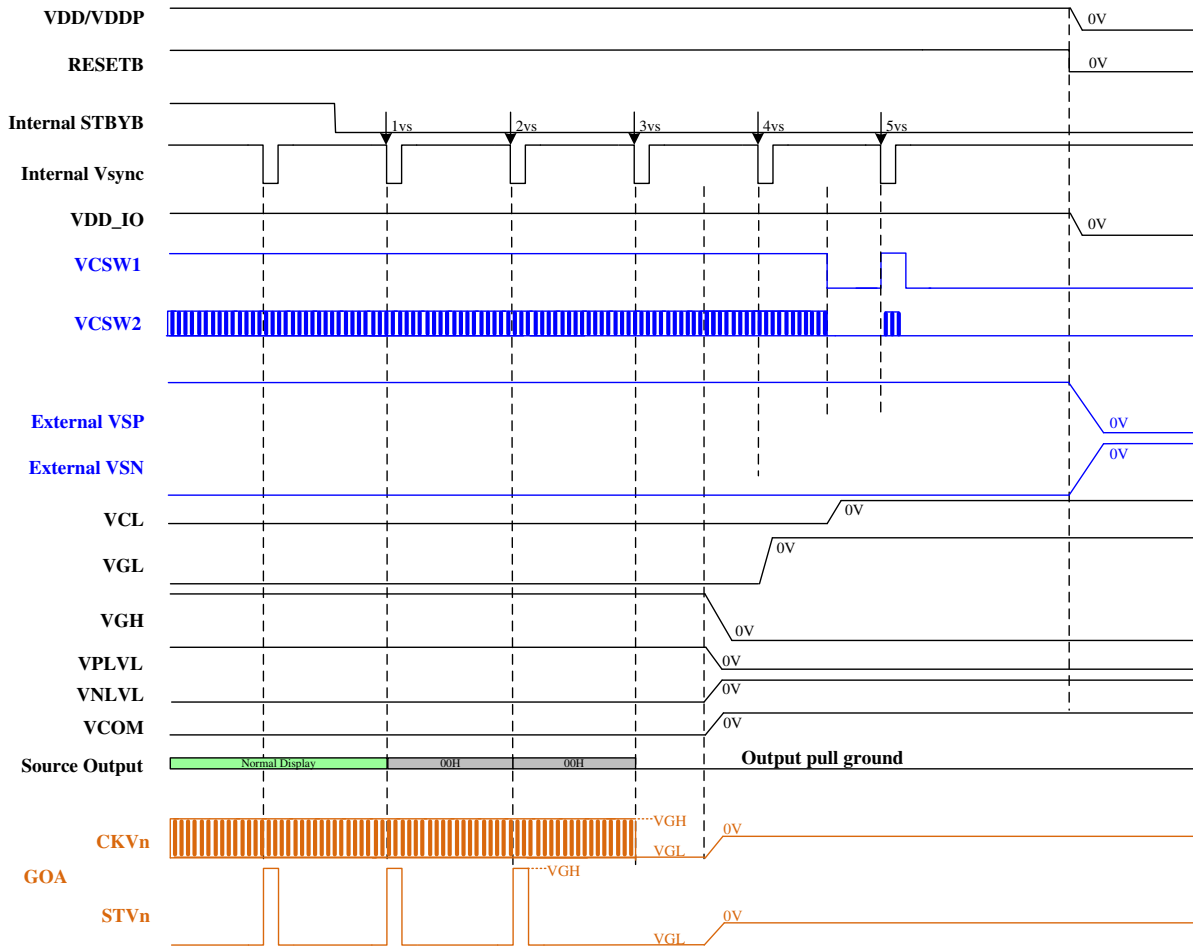


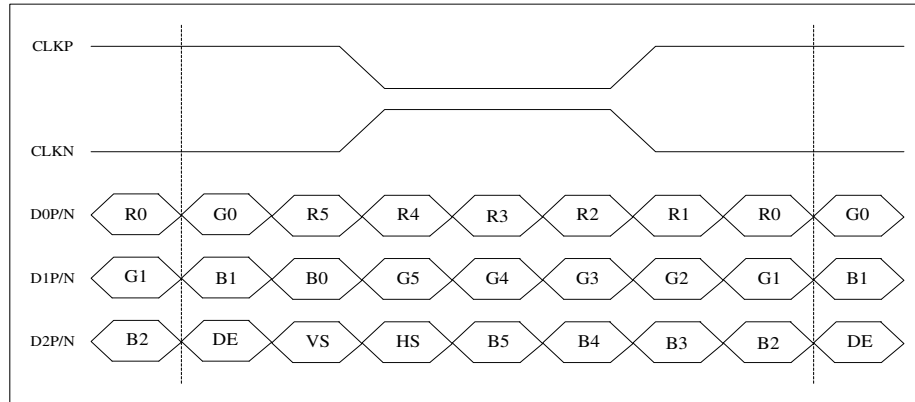
Figure 5.2: Power off sequence with PMODE[1:0]=00b



6. INTERFACE

6.1 MIPI interface

6.1.1 Data input format for MIPI





7.1 MIPI Input Timing Table

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60Hz	F _{DCLK}		71.9		MHz
HSYNC period time	T _H		920		DCLK
Horizontal display area	T _{HD}		800		DCLK
HSYNC pulse width	T _{HPW}		24	-	DCLK
HSYNC back porch	T _{HBP}		24	-	DCLK
HSYNC front porch	T _{FBP}		72	-	DCLK
VSYNC period time	T _V		1304		H
Vertical display area	T _{VD}		1280		H
VSYNC pulse width	T _{VPW}		2	-	H
VSYNC back porch	T _{VBP}		10	-	H
VSYNC front porch	T _{VFP}		12	-	H



8.0 MIPI mode AC electrical characteristics

Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
Clock frequency	R_{xFCLK}	30	-	TBD	MHz	Refer to input timing table for each display resolution
Input data skew margin	T_{RSKM}	500	-	-	ps	$ VID = 200mV$ $RxVCM = 1.2V$ $RxFCLK = 81MHz$
Clock high time	T_{LVCH}	-	$4/(7 * R_{xFCLK})$	-	ns	
Clock low time	T_{LVCL}	-	$3/(7 * R_{xFCLK})$	-	ns	
PLL wake-up time	T_{enPLL}	-	-	150	us	

Table 13.1: MIPI mode AC electrical characteristics

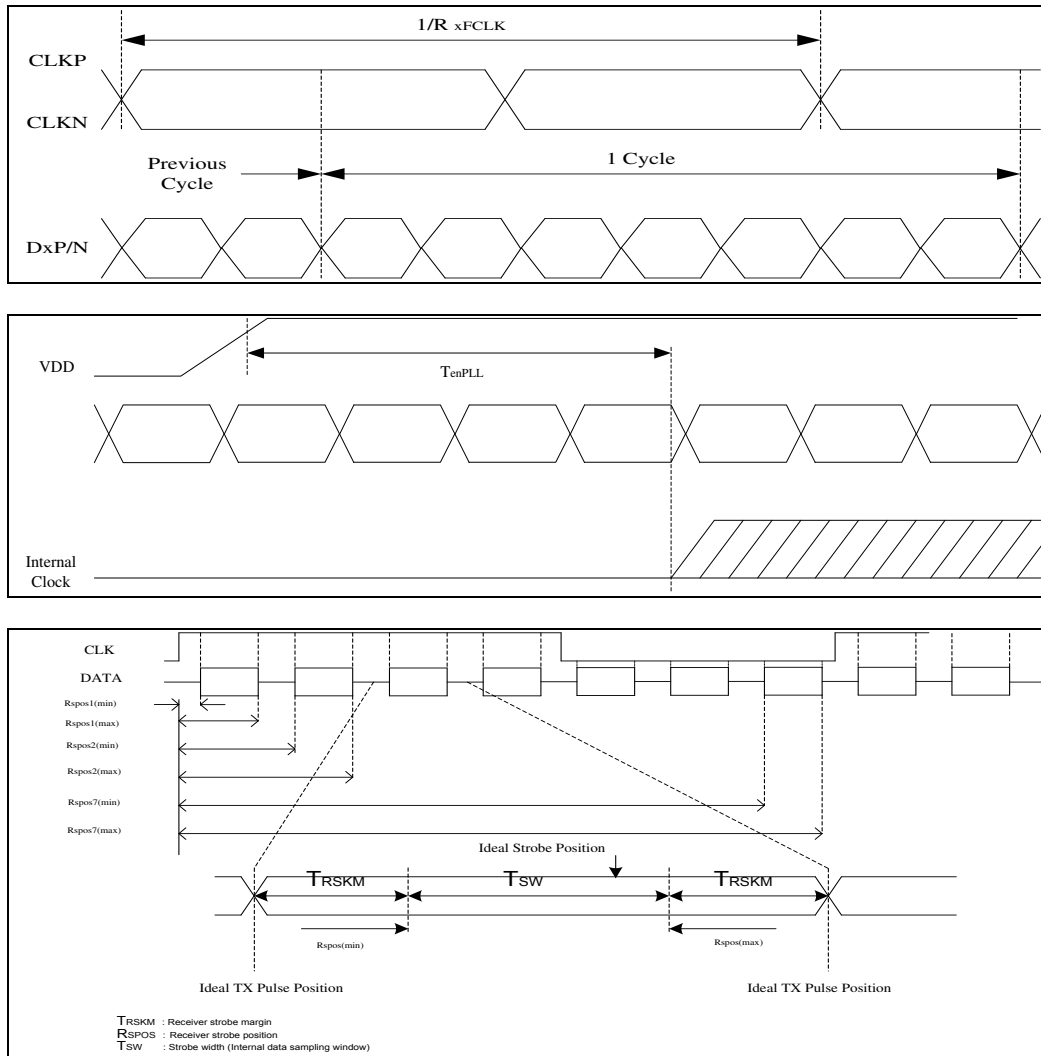


Figure 13 Mipi .3 figure

Parameter	Symbol	Min.	Typ.	Max.	Units	Condition
Modulation Frequency	SSC_{MF}	23	-	93	KHz	
Modulation Rate	SSC_{MR}	-	-	+3	%	

Table 12.2: SSC table



9.0 Panel Structure

9.1. Driving method for panel structure

This panel can support 2 types of driving method — stripe and zigzag. User could control Register: ZIGZAG, Z_LEFT, Z_SCAN, and Z_TYPE select Panel type as following Figure:

Normal driving method for panel structure

Normal driving method: ZIGZAG = 0

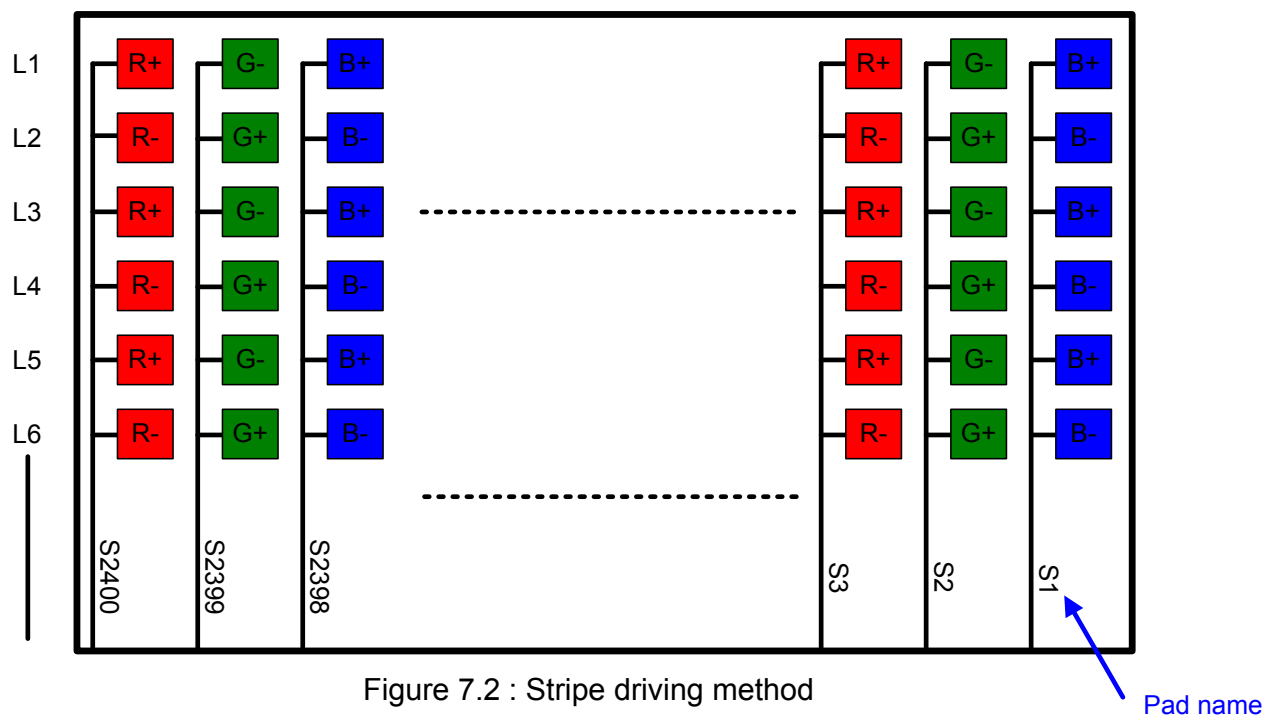


Figure 7.2 : Stripe driving method

Pad name



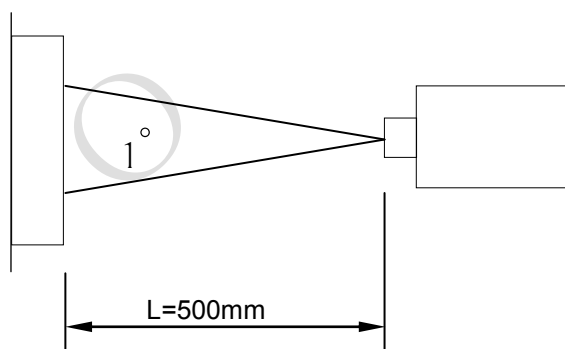
10.0. OPTICAL SPECIFICAT ION

(Taransmittance, contrast ratio, response time, viewing angle results are using AYF LC + Polarizer + Corresponding Backlight, reference only)

Ambient condition : $25 \pm 2^{\circ}\text{C}$, $60 \pm 10\% \text{ RH}$, under 10 Lunx in the darkroom

ITEM		SYMBOL		CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK		
Transmittance (With Polarizer)		T _{PL}		ϑ = ϕ = 0°	4.4	4.9	--	%	Note 1 Base on AYF Pol condition & AYF BL		
Contrast Ratio		CR			600	800	---		Note 2		
Response Time		Tr+Tf		ϑ = ϕ = 0°	---	30	35	ms	Note 3		
Viewing angle	Vertical	U	θ*2	CR ≥ 10	80	85		degree	Note 4		
		D			80	85		degree			
	Horizontal	L	ϕ*2		80	85		degree			
		R			80	85		degree			
Color Filter Chromaticity	W	x		ϑ = ϕ = 0°	0.276	0.296	0.316		Note 1 (Base on C Light)		
		y			0.302	0.322	0.342				
	R	x			0.601	0.621	0.641				
		y			0.294	0.314	0.334				
	G	x			0.252	0.272	0.292				
		y			0.560	0.580	0.600				
	B	x			0.121	0.141	0.161				
		y			0.093	0.113	0.133				
	NTSC						60				

Note 1. .Measure device : BM-5A (TOPCON) , viewing cone= 1° , $I_L=20\text{mA}$ °

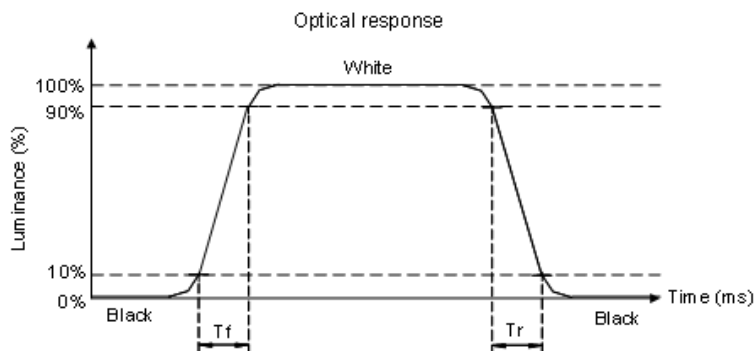


Note 2. Definition of Contrast Ratio :

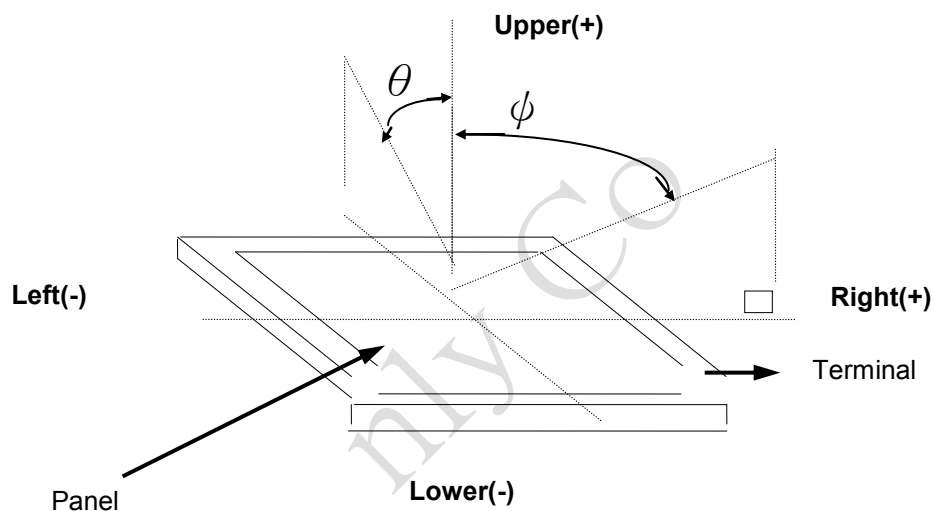
CR = White Luminance (ON) / Black Luminance (OFF)



Note 3. The output signals of photo detector are measured when the input signals are changed from “black” to “white” (rising time) and from “white” to “black” (falling time), respectively.
Definition of response time : The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 4 Definition of view angle(θ , ϕ) :





NO.	TEST ITEM	CONDITIONS
1	HighTemperature Operation	70° C , 240 hrs
2	Low Temperature Operation	-20° C , 240 hrs
3	High Temperature and High Humidity Operation	60 ° C , 90% RH, 240 hrs

NOTE

1. All judgement of display are performed after temperature of panel return to room temperature.
2. Display function should be no change under normal operating condition.
3. Under no condensation of dew.
4. AYF only guarantee the above 3 test items, and without guarantee the others.
5. Judgment Standard:

The Judgment of the above test should be made as follow:

Pass: Normal display image and no line defect.Partial transformation of the module parts should be ignored.

Fail: No display image, function NG, or line defects.

WARRANTY

- 8.1 The period is within 3 months since the date of shipping out under normal using and storage conditions.
- 8.2 The warranty will be avoided in case of defect induced by customer.