

6-18GHz/51dBm/Module

Model Number: KB60180M51B

The model KB60180M51B is a multi-octave high power amplifier operating between 6 GHz and 18 GHz and offering a wide dynamic Range with 51 dBm typical saturated power. The employment of gallium nitride (GaN) and chip-and-wire technology in manufacturing ensures this module state-of-the-art power performance with excellent power-to-volume ratio. It is ideal for jamming, EMC, test and measurement applications.

FEATURES:

- Solid-state design
- Instantaneous ultra-broadband
- 50Ohms input and Output matched
- Built-in protection circuits

ELECTRICAL SPECIFICATIONS @ +28.0VDC, 25°C, 50Ω

Parameter	Symbol	Minimum	Typical	Maximum	Units
Operating Frequency	BW	6000		18000	MHz
RF Output Power @P _{in} =0dBm	PSAT		51		dBm
Power Gain @PSAT	Gp		51		dB
Power Gain Flatness	Δ Gp		±2		dB
Input Return Loss	S ₁₁		-10		dB
Harmonics @100W	H		-15		dBc
Spurious Signals	Spur		-55		dBc
In/Output Impedance	Impedance		50		Ω
Operating Voltage	V _{DC}	26	28	30	Volt
DC Current	I _{DD}		42	48	Amp

MECHANICAL SPECIFICATIONS

Parameter	Value	Units	Limits
Dimensions	320x230x120[12.6x9.06x4.72]	mm [inch]	
Weight	10 [22.05]	kg [lbs]	Maximum
RF Connectors Input	SMA, Female		
RF Connectors Output	N, Female		
DC Interface Connector	J29A-37ZKP		
Cooling	With heatsink,forced air cooling		

ENVIRONMENTAL CHARACTERISTICS (Design to Meet)

Parameter	Minimum	Typical	Maximum	Units	Notes
Operating Temperature	-40		60	°C	
Non-operating Temperature	-45		85	°C	Storage
Relative Humidity (non-condensing)	5		95	%	

*Focus on Your Application****Chengdu KeyLink Wireless Technology Co., Ltd.***

>> 5-2, Huidu Headquarters Park, High-Tech(West) Zone, Chengdu, 611731, P. R. China

>> Phone: +86(28)87870761*8708 >> Fax: +86(28)87883266

>> Email: sales@keylinkmw.com >> www.keylinkmw.com

Specifications subject to change without notice

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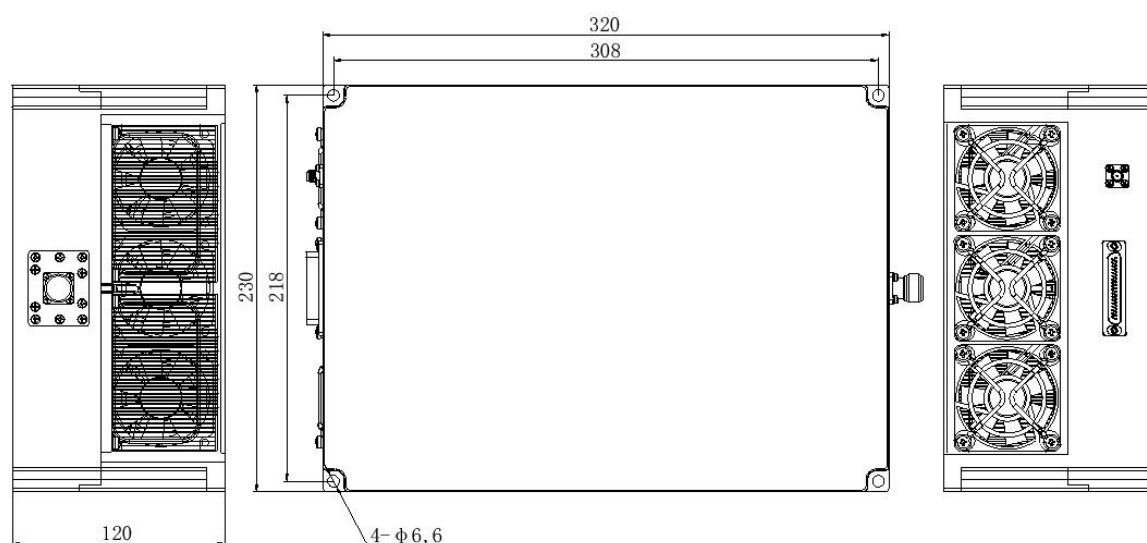
Absolute Maximum Rating

Input RF drive level without damage	+5dBm	Maximum
Load VSWR @ POUT =80W	2.5:1 @ all load phase & amplitude continuous More than 3:1 may cause PA damage	
Over Temperature	80°C @ heatsink	

DC INTERFACE CONNECTOR

1-15	VDD	28 V
16-31	GND	Ground
32	ENABLE	Amplifier Enable: TTL Logic High (3.3V)
33	GND	Ground
34	CURRENT SENSE	Analog voltage relative to IDD
35	TEMP SENSE	Analog voltage relative to Module's Temperature
36	RESET	It is usually connected to a high-impedance state, and to a low level (0V) when it is necessary to cancel standing wave protection
37	ALARMS	TTL Logic High (2V~3V)

OUTLINE DRAWING [All dimensions in mm]



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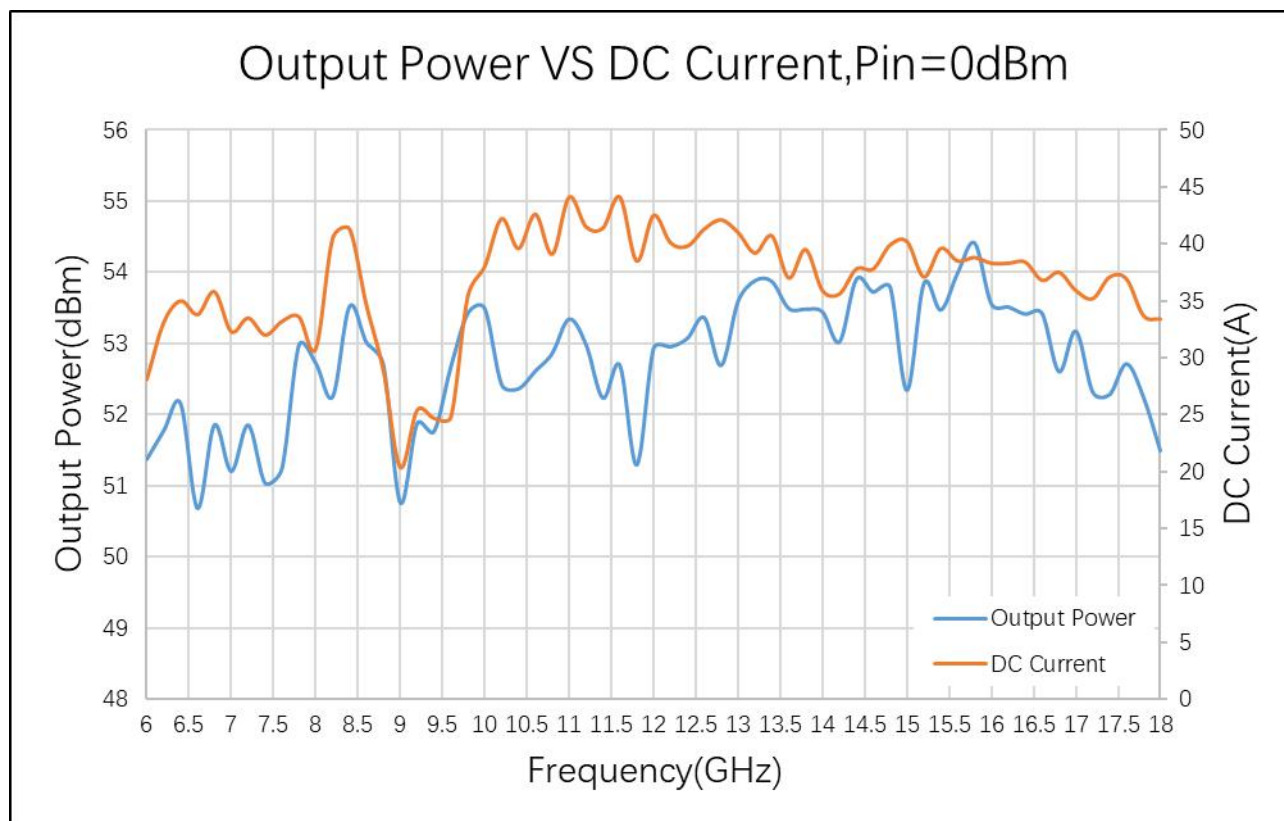
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TYPICAL PERFORMANCE PLOTS (For reference only)

**Note:**

This product will be developed upon contract/order. All the above specs are for reference only. Actual product may be different. Please check with KeyLink sales team if you have any question.

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