DMG19108K133_05WTC

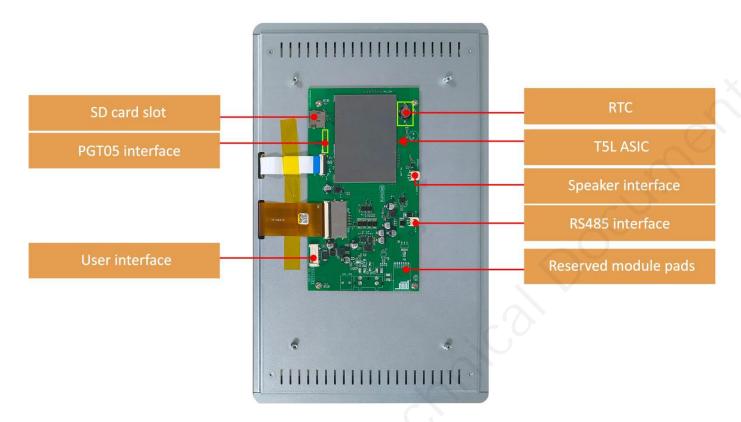
Features:

- Based on the T5L2 ASIC CPU, running the DGUS II human-machine interaction software platform,
 smart LCM for medical-grade applications.
- 13.3-inch, 1920*1080 pixels, 262K colors true-color display, IPS LCD screen, 2K HD smart screen.
- With RTC and speaker interface.
- OCA bonded capacitive touch panel.



1. Hardware and interface

1.1 Hardware interface



Hardware interface



1.2 Hardware and interface description

No.	Name	Description
1	T5L2 ASIC*3	DWIN independently developed, mass production in 2019; patented encryption technology ensures code and data security; low power consumption, strong anti-interference capability, easily passes EMC/EMI tests with dual-sided PCB design
2	User interface	8Pin_2.0mm socket for power supply and serial communication. Download rate(typical value): 12KByte/s
3	Flash	32MBytes (2*16MBytes NOR Flash), can be used to store user UI files such as fonts, images, music, etc., with erase/write cycles >100,000 times
4	Expand Flash pads	Two expansion slots are available, supporting expansion of NOR Flash and NAND Flash. The maximum expansion for NOR Flash is up to 64Mbytes. When combining NOR Flash and NAND Flash, the maximum expansion is up to 48Mbytes (using one expansion slots) + 512Mbytes
5	RTC	Super-capacitor supplies power to RTC, accuracy: ±20ppm @25°C. Can maintain normal operation for 7 days after power-off
6	Speaker interface	2Pin_2.0mm socket, external speaker interface
7	SD card slot	Supports downloading of all files (user UI files, CFG files, underlying kernel firmware), displays download statistics on the screen, download rate: 4 Mb/s. When downloading files, the SD card needs to be formatted in FAT32 format, with a recommended allocation unit size of 4096
8	Reserved module interface	Supports soldering of WI-FI module and USB download module. WI-FI module model: WI-FI-10; USB module models: HDL702, HDL703
9	PGT05 interface	Used for reprogramming the underlying DGUS firmware



2. Specification parameters

2.1 Display parameters

LCD Type	IPS process TFT display screen
Viewing Angle	Wide viewing angle (typical values are 85°/85°/85°/85°), high contrast, and good color reproduction
Resolution	1920×1080 pixels (0°/90°/180°/270°)
Color	262K color(18-bit 6R6G6B)
Active Area (A.A.)	293.76mm (W)×165.24mm (H)
View Area (V.A.)	293.76mm (W)×165.24mm (H)
Interface	eDP
Backlight Mode	LED
Backlight Service Life	>10,000 hours (Time of the brightness decaying to 50% on the condition of continuous working with the maximum brightness)
Brightness	200nit
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)
Note:You can use dynan	nic screen saver wallpapers to avoid afterimages caused by fixed page display

Note: You can use dynamic screen saver wallpapers to avoid afterimages caused by fixed page display for a long time.

2.2 Touch parameters

Туре	CTP (Capacitive touch panel)			
Interface	I ² C			
Structure	G+G structure with surface cover of tempered glass			
Touch Mode	Single point touch and support continuous sliding touch			
Surface Hardness	≥6H			
Light Transmittance	>85%			
Life	>10,000H			



2.3 Serial interface parameters

Mode	UART2: RS232 UART4: RS232 (Only available after OS configuration) UART5: RS485 (Only available after OS configuration)						
	Test Condition	Min	Тур	Max	Unit		
UART2,4	Output 1	2.5	5.0	-	V		
Voltage Level	Output 0	-	-5.0	-2.5	V		
	Input 1	-15.0	-5.0	-	V		
	Input 0	-	5.0	15.0	V		
UART2,4 Baud Rate	3150~3225600bps, typ	3150~3225600bps, typical value of 115200bps					
UART5	Test Condition	Min	Тур	Max	Unit		
	Output 1	2.5	5.0	-	V		
Voltage Level	Output 0	-	-5.0	-2.5	V		
	Input 1	0	2.5	-	V		
	Input 0	- (-2.5	-0.2	V		
UART5 Baud Rate	3150~921600bps, typid	3150~921600bps, typical value of 115200bps					
Data Format		UART2: N81 UART4: N81/E81/O81/N82; 4 modes (OS configuration) UART5: N81/E81/O81/N82; 4 modes (OS configuration)					
Interface Cable	8Pin_2.0mm (RS232) 2Pin_2.0mm (RS485)	8Pin_2.0mm (RS232)					



2.4 Electrical specifications

Rated Power	<8W	<8W			
Operating Voltage	12~36V, typical v	12~36V, typical value of 12V			
One westimes Command	560mA	VCC=12V, max backlight			
Operating Current	160mA	VCC=12V, backlight off			
Recommended power supply: 12V 1A DC					

2.5 Operating environment

Operating Temperature	0℃~50℃ (12V @ 60% RH)
Storage Temperature	-20℃~60℃
Conformal Coating	None
Operating Humidity	10%~90%RH, typical value of 60% RH



3. Reliability test

3.1 Electrostatic discharge test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: Place the product on the testing fixture of the test bench (fixture height approximately 15cm), and conduct contact discharge and air discharge tests on the smart screen, during the experimental process, it was observed whether the screen is dead, black, white, splash, or reboot. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.

■ Test standard : □EN 61000-4-2:2009 □IEC 61000-4-2:2008 □GB/T 17626.2-2018 □Other:

Table 1: Electrostatic Discharge Immunity (Air Discharge)

T D	Test Levels							
Test Points Locations	-2kV	+2kV	-4kV	+4kV	-8kV	+8kV	-15kV	+15kV
Screen							13	R
/	/	/	/	/	1	1	1	1
1	/	1	/	/	1	1	1	1

Table 2: Electrostatic Discharge Immunity (Direct Contact)

Test Points Locations	Test Levels								
rest Points Locations	-2kV	+2kV	-4kV	+4kV	-6kV	+6kV	-8kV	+8kV	
Frame							R	D	
/	1	/	/	1	1	1	1	7	
1	1	1	1	1	/	1	1	1	

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3.2 EFT test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and the smart screen is energized by the power supply coupled with a EFT generator. During the experimental process, it was observed whether abnormal reset, display or touch phenomena occurs. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.

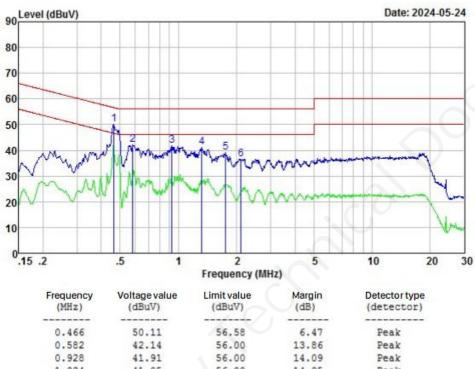
Test Points		Test Levels(kV)							
iest i	roints	-0.5	+0.5	-1.0	+1.0	-2.0	+2.0	-4.0	+4.0
	L					12	13		
0/	N					/3	12		
<u> </u>	Earth	/	/	1	1	1	1	1	/
Power ports	L+N					Δ	19		
	L + Earth	/	1	/	1	1	1	1	/
	N + Earth	/	1	/	1	1	1	1	1
	L+N+Earth	1	1	/	1	1	1	1	/
Signal ports		1	1	/	1	/	/	1	/



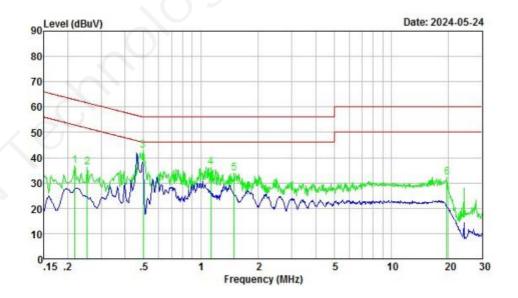
3.3 CE test

Test Item	Test Standard	Result
CE	ClassB-6dB	Normal operation

NEUTRAL



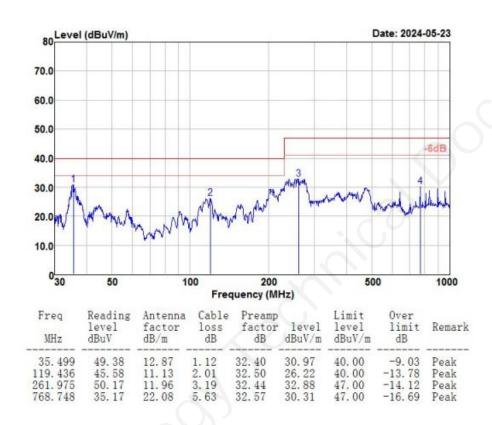
Voltage value (dBuV)	Limit value (dBuV)	Margin (dB)	(detector)
50.11	56.58	6.47	Peak
42.14	56.00	13.86	Peak
41.91	56.00	14.09	Peak
41.05	56.00	14.95	Peak
39.18	56.00	16.82	Peak
36.55	56.00	19.45	Peak
	(dBuV) 	(dBuV) (dBuV) 50.11 56.58 42.14 56.00 41.91 56.00 41.05 56.00 39.18 56.00	(dBuV) (dBuV) (dB) 50.11 56.58 6.47 42.14 56.00 13.86 41.91 56.00 14.09 41.05 56.00 14.95 39.18 56.00 16.82



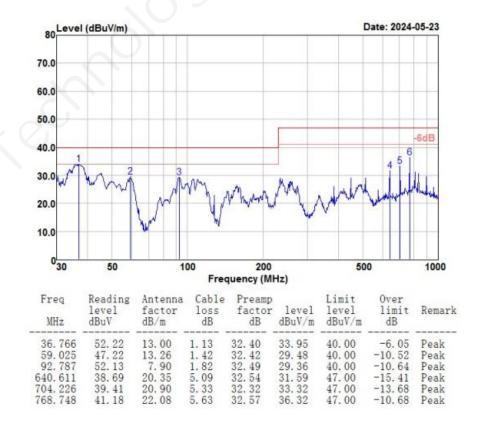
3.4 RE test

Test Item	Test Standard	Result
RE	ClassB-6dB	Normal operation

HORIZONTAL



VERTICAL





3.5 CS test

•	Test standard	I: □EN 61000-4-6:2014	☐ IEC 61000-4-6:2013	■ GB/T 17626.6-2	2017
		☐ Other:			
		4 11 1 000/ 4111		2004 011	

■ Modulation: ■ Amplitude 80%,1kHz sine wave ☐ Amplitude 80%,2Hz sine wave ☐ Other:

■ Dwell time: ■ 1s □3s □other:

■ Frequency Step Size : ■ 1 % of preceding frequency value ☐ other:

Coupling Line	Frequency Range	Voltage Level(e.m.f.)	Result
	(MHz)	(V)	
Power terminal	0.15-80	10	Α

3.6 SURGE test

■ Test standard : □EN 61000-4-5:2014+A1:2017 ☑IEC 61000-4-5:2014+A1:2017 □GB/T 17626.5-2019 □Other:

Table 1: DL mains power input port

Level	Voltage	Polarity	Path	Result
1	0.5kV	±		
2	1kV	±	Positive to negative	1
3	2kV	±	0.1	/
4	4kV	±	/	/

Table 2: _____ I/O Circuit and Lines

Level	Voltage	Polarity	Path	Result
1	0.5kV	±	Line-Ground	/
2	1kV	±	Line-Ground	/
3	2kV	±	Line-Ground	/
4	4kV	±	Line-Ground	1

3.7 RS test

■Test standard :	□EN 610	00-4-3:2006+A1:2008+A2:2010 ☑IEC 61000-4-3:2006+A1:2007+A2:2010
	□GB/T 1	7626.3-2016 □other:
■ Modulation: ☑/	Amplitude	80%,1kHz sine wave ☐ Amplitude 80%,2Hz sine wave ☐ Other:
■ Dwell time: ☑1	s □3s	□other:
Frequency Step	Size : ☑1 9	% of preceding frequency value □other:

Frequency range	Field strength	Front side		Rear side		Left side		Right side	
(MHz)	(V/m)	VER	HOR	VER	HOR	VER	HOR	VER	HOR
80-1000	10	A	1	A	A	A	A	A	1
1400-3000	3	8	8	D	10	18	10	A	1

Performance Criterion:

- A. Normal performance within limits specified by the manufacturer, requestor or purchaser;
- B. Temporary loss of function or degradation of performance which ceases after the disturbance ceases, and from which the equipment under test recovers its normal performance, without operator intervention;
- C. Temporary loss of function or degradation of performance, the correction of which requires operator intervention;
- D. Loss of function or degradation of performance which is not recoverable, due to damage to hardware or software, or loss of data.

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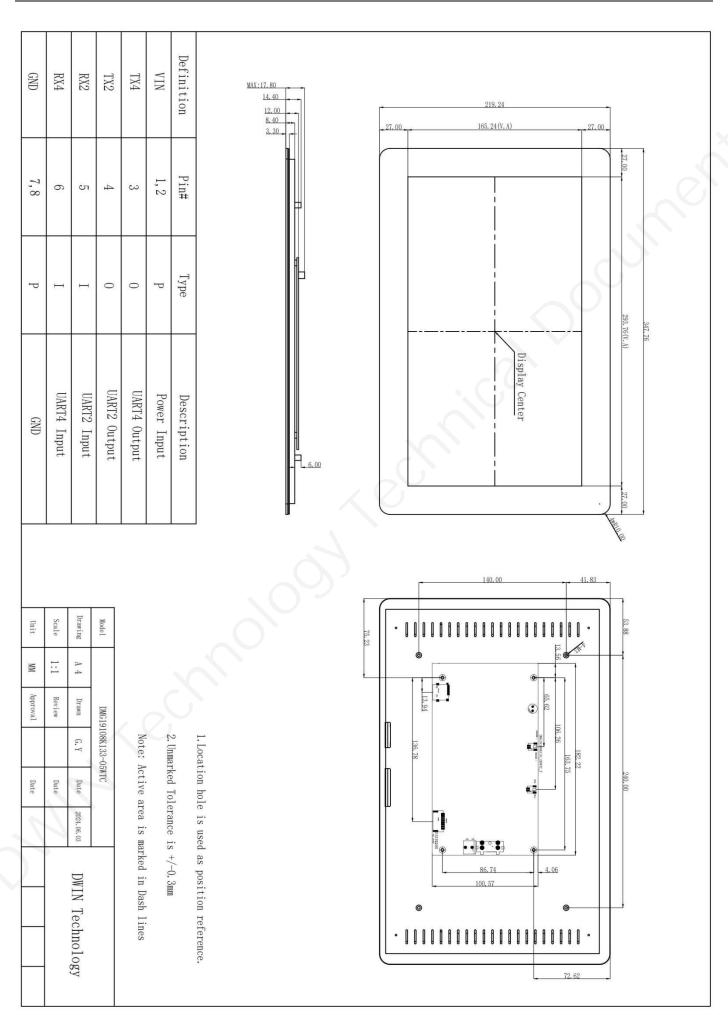


4. Packaging & dimensions

Form Factor	347.76(W) ×219.24(H) ×17.80(T) mm
Net Weight	1320g

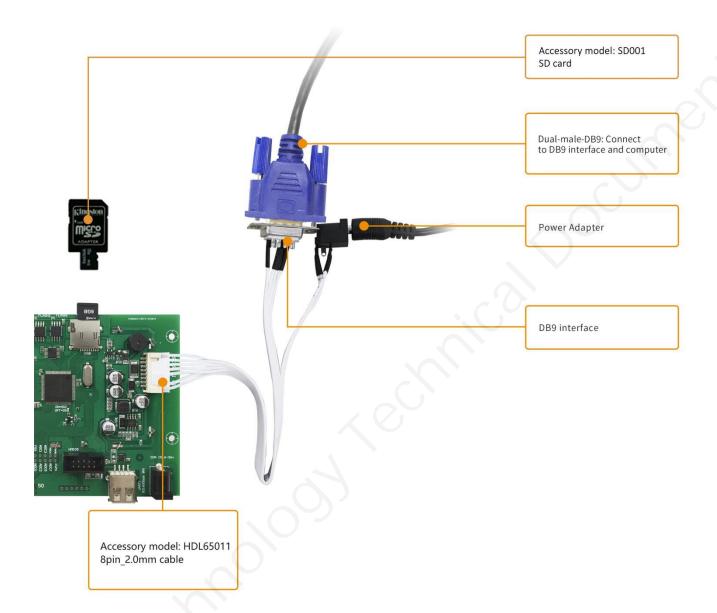
Packaging Standards

Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)
Carton1:	525mm(L)×330mm(W)×80mm (H)	1	1	1



5. Debugging tools

It is recommended for new users of DWIN smart LCMs to purchase official accessories. For more details, please refer to customer service center.



6. T5L series IC features

- (1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle)high speed operation.
- (2) Separate GUI CPU Core running DGUS II System:
 - High-speed display memory, 2.4GB/S bandwidth.
 - 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
 - Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
 - Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
 - 1-way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
 - 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
 - Support DGUS development and simulation on PC. Support background remote upgrade.
- (3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:
 - Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
 - 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
 - 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channel 16-bit PWM of adjustable resolution.
 - Support IAP on-line simulation and debugging with unlimited number of breakpoints.
 - Upgrade code online through DGUS system.
- (4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- (5) Operating temperature ranges from -40°C to +85°C(IC operating temperature customizable from -55°C to 105°C).

DWIN encourages users to design your own customized product based on T5L



7. Revision records

Rev	Revise Date	Content	Editor
00	2024-07-22	First Edition	Xu Ying

Please contact us if you have any questions about the use of this document or our products, or if you would like to know the latest information about our products:

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!



Important Disclaimer

DWIN reserves the right to make any changes to product designs without prior notice.

Customers should ensure strictly adhering to all the relevant standards and requirements during the product application process, including but not limited to functional safety, information security, and regulatory provisions.

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